

Fig. 1

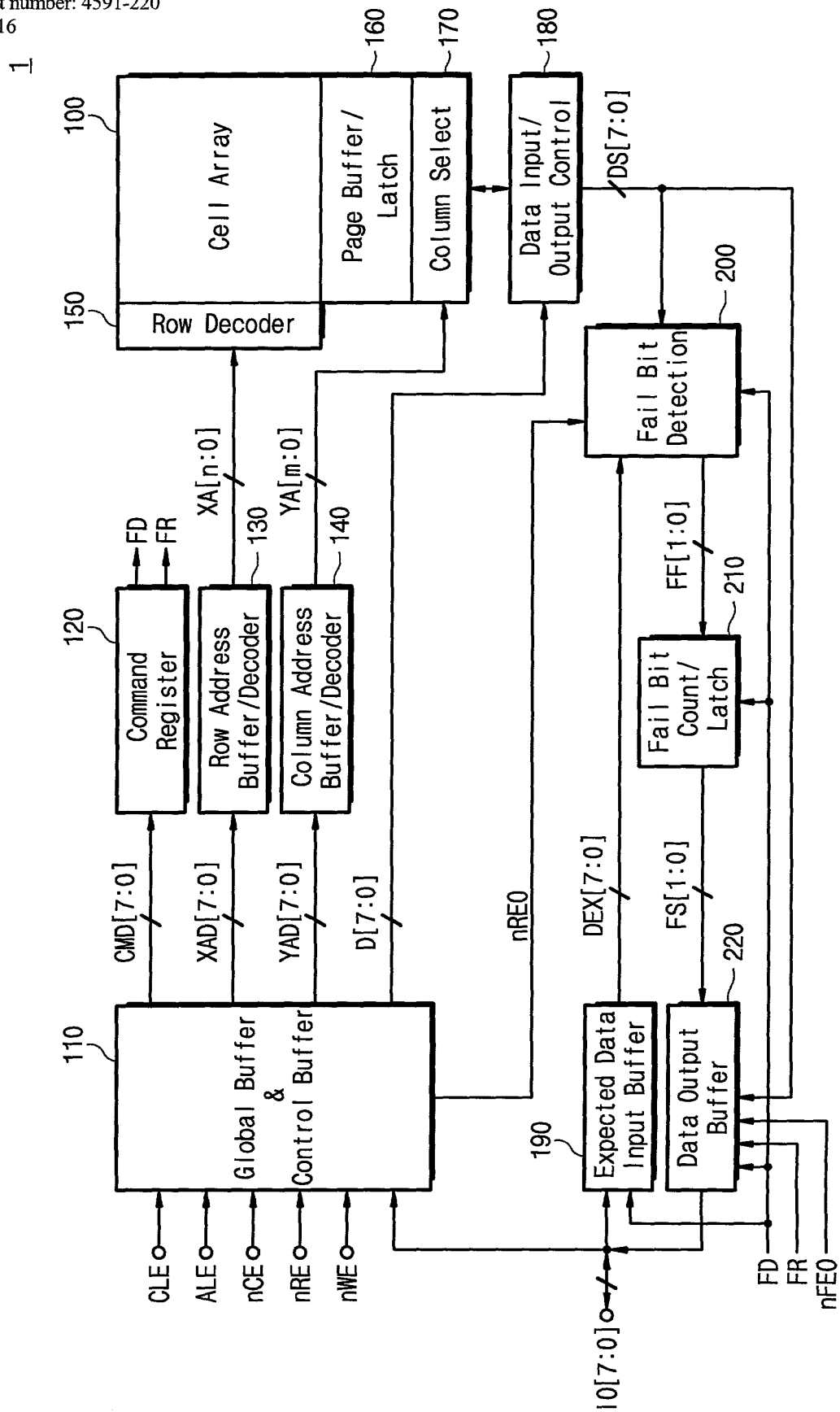
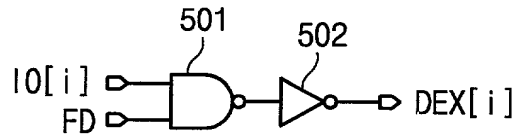
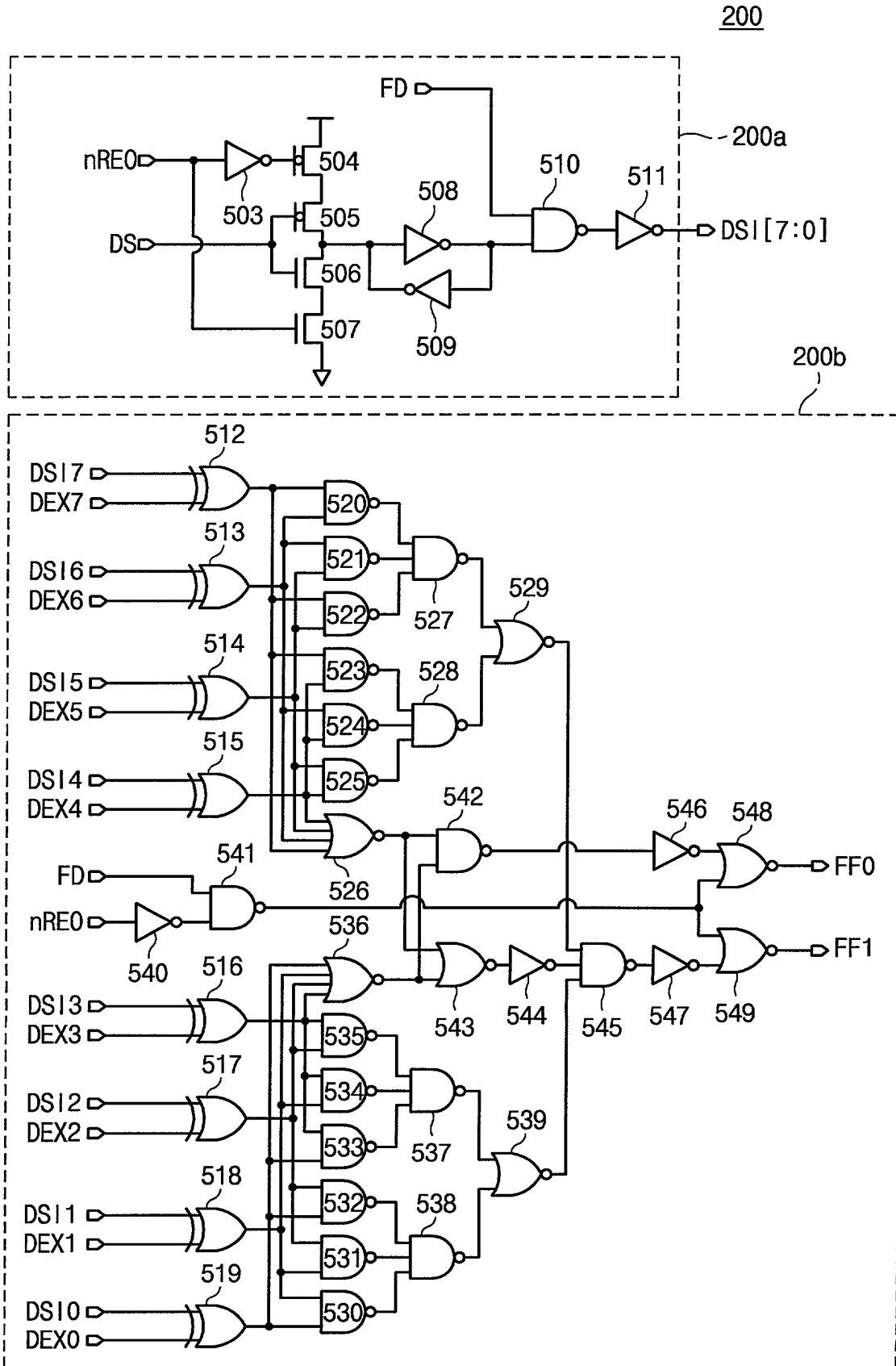


Fig. 2

190



**Fig. 3**



**Fig. 4**

210

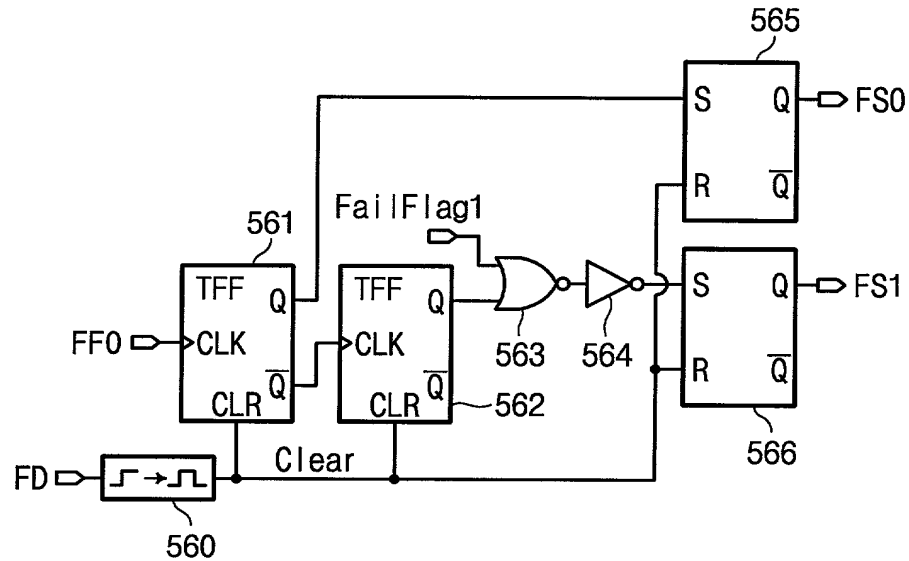


Fig. 5

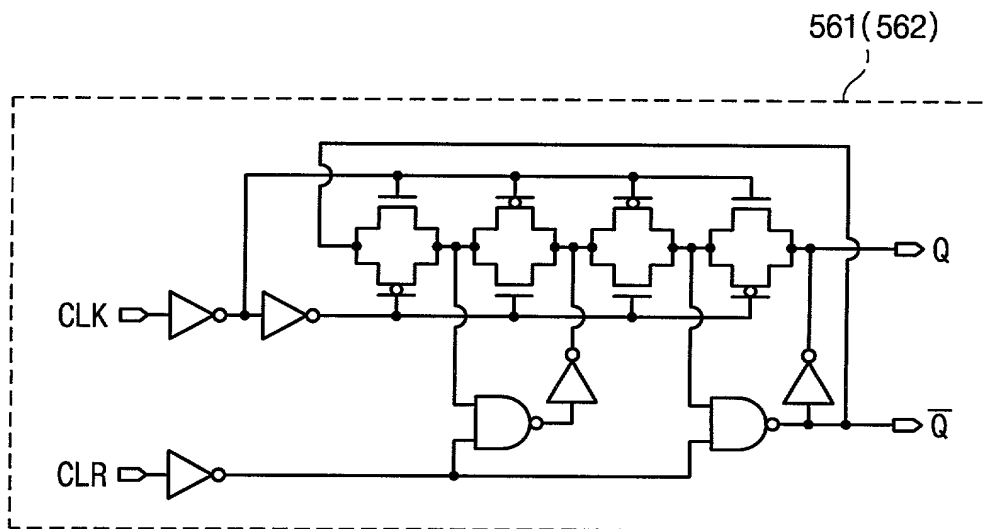


Fig. 6

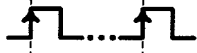
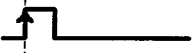











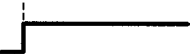


Number of Fail Bits		More than 2 Bits		1 Bit	none
Input	FailFlag0				
	FailFlag1				
Output	FailStatus0				
	FailStatus1				

Fig. 7

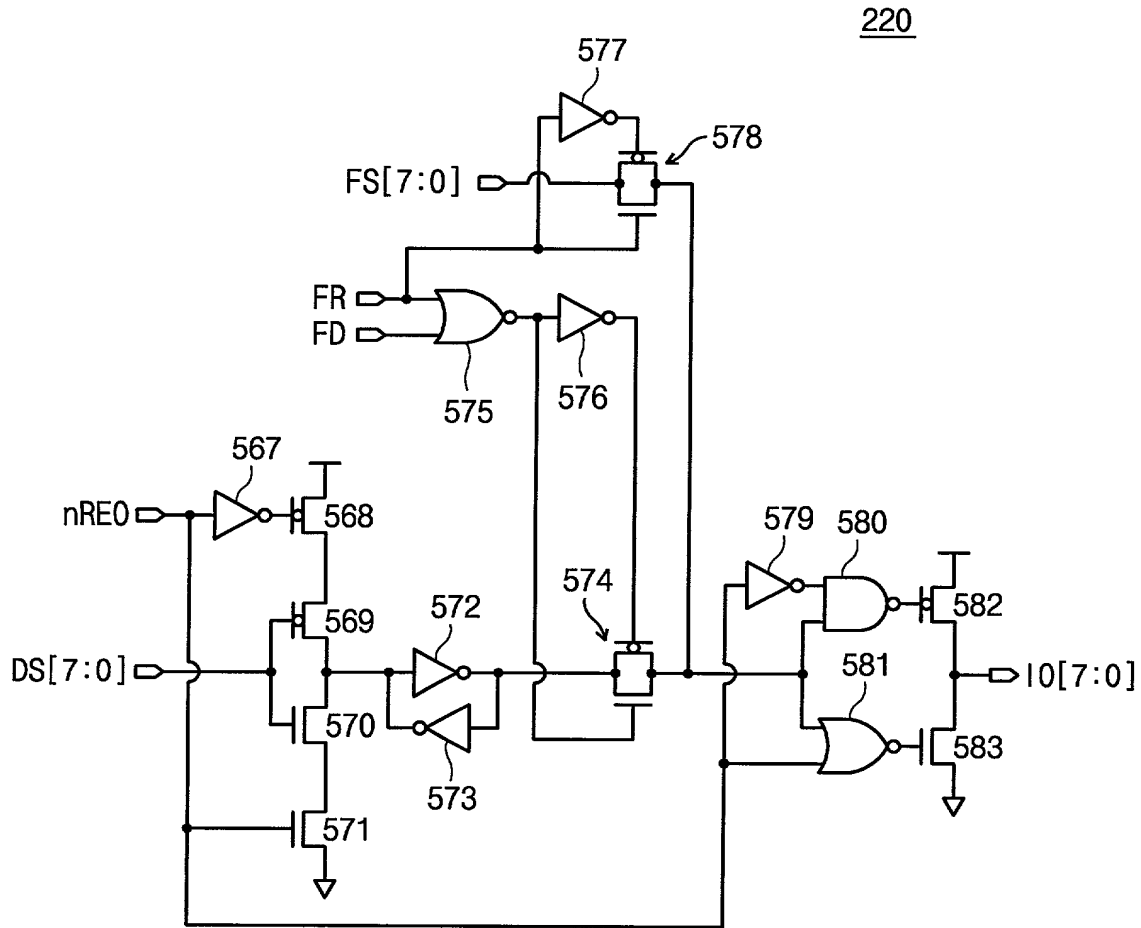


Fig. 8

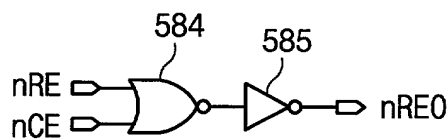


Fig. 9

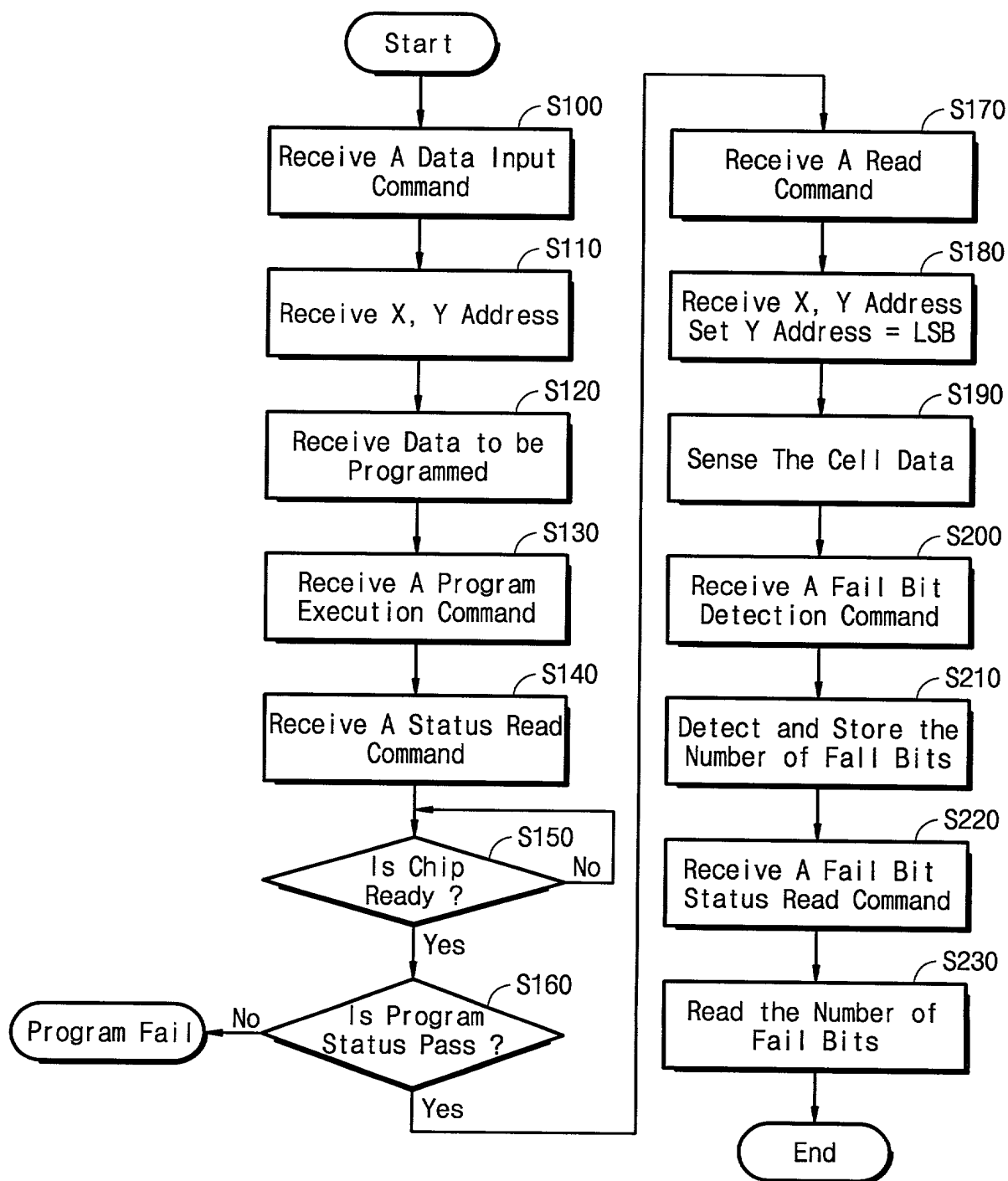


Fig. 10

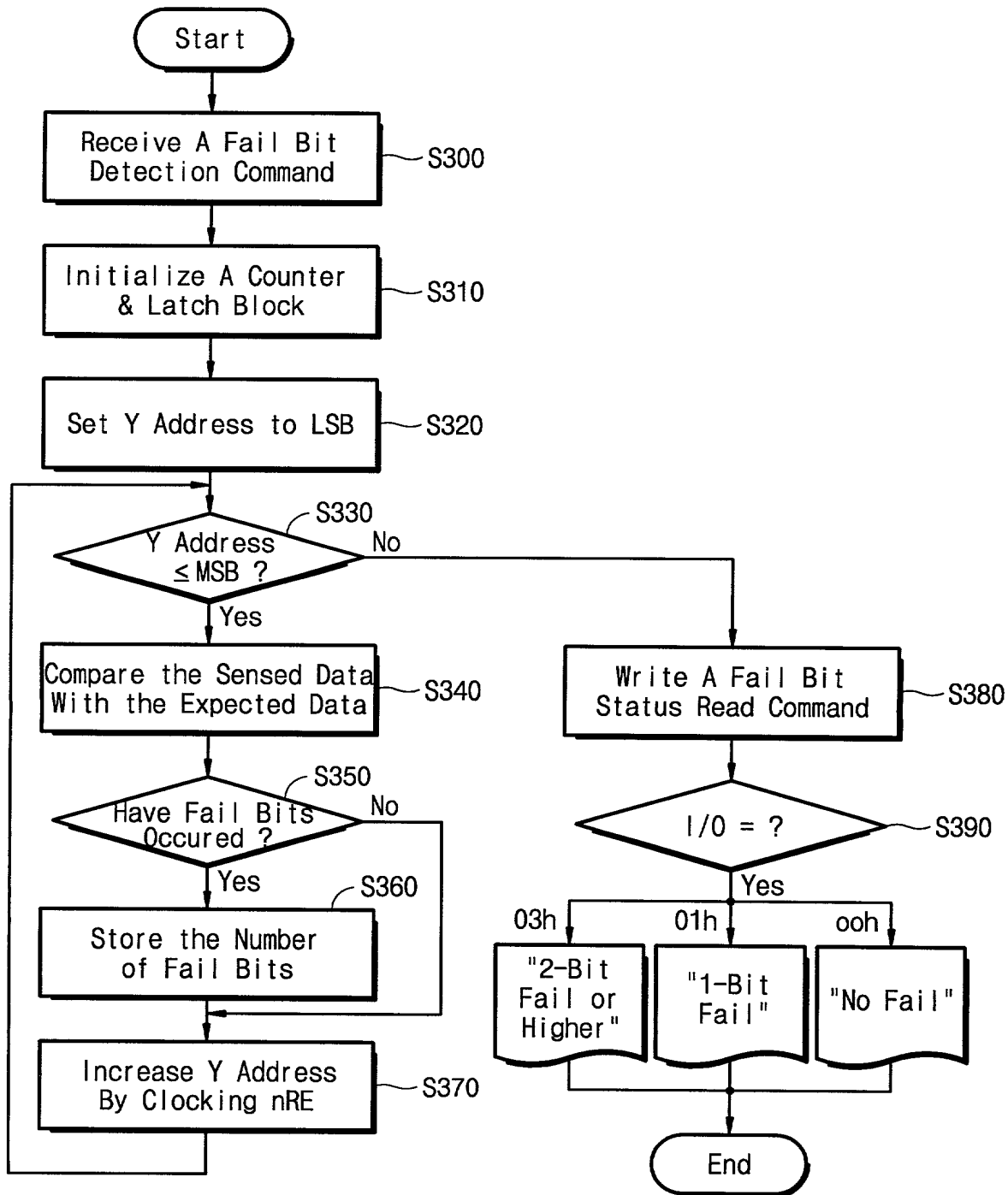




Fig. 11A

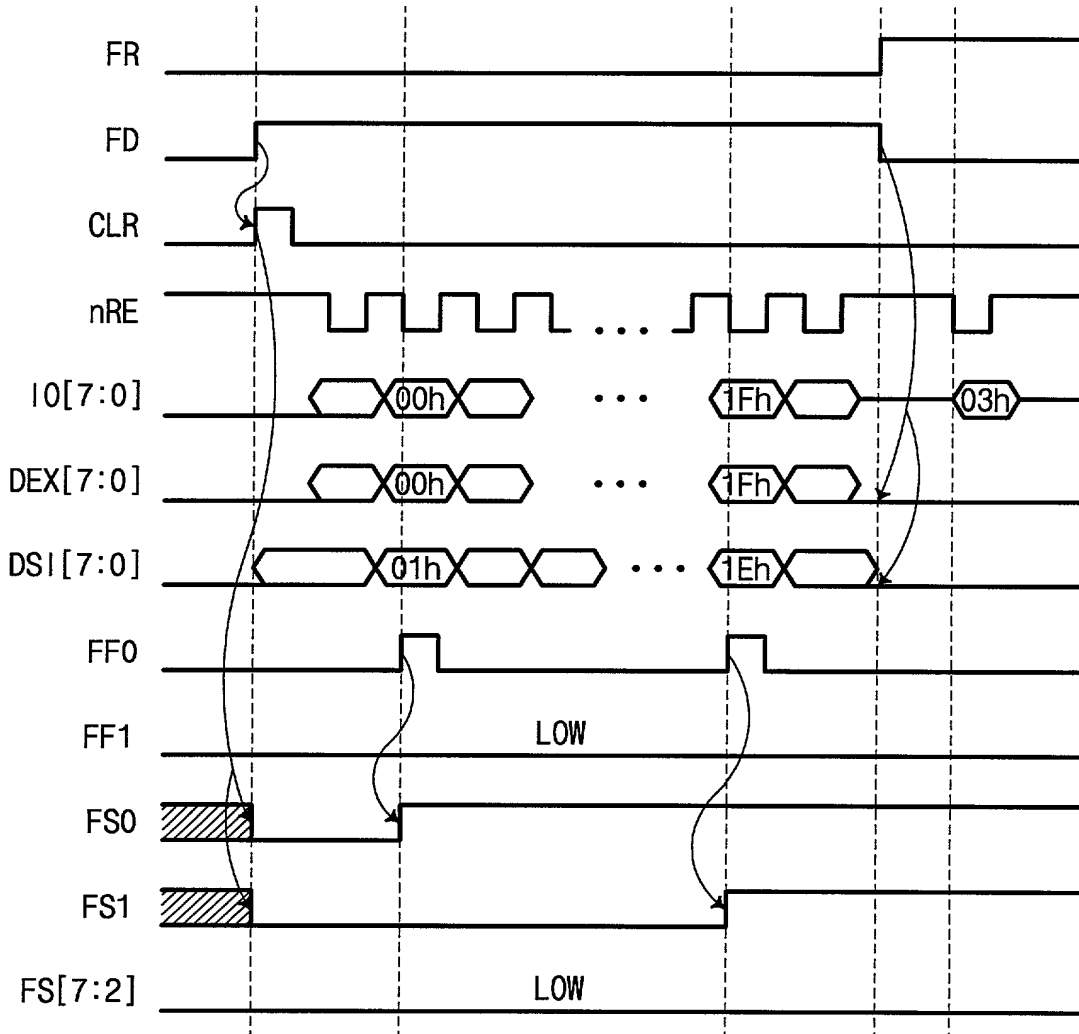


Fig. 11B

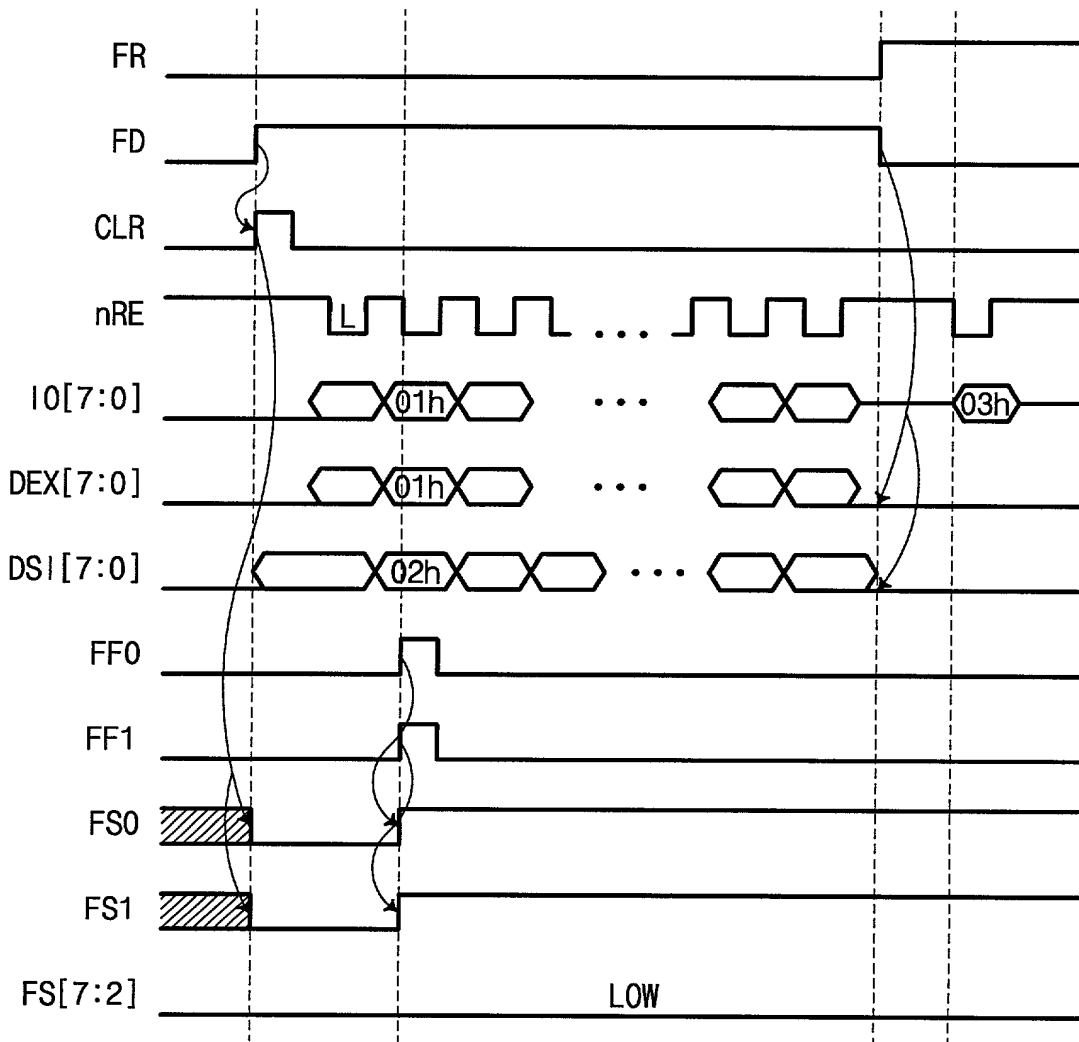


Fig. 12

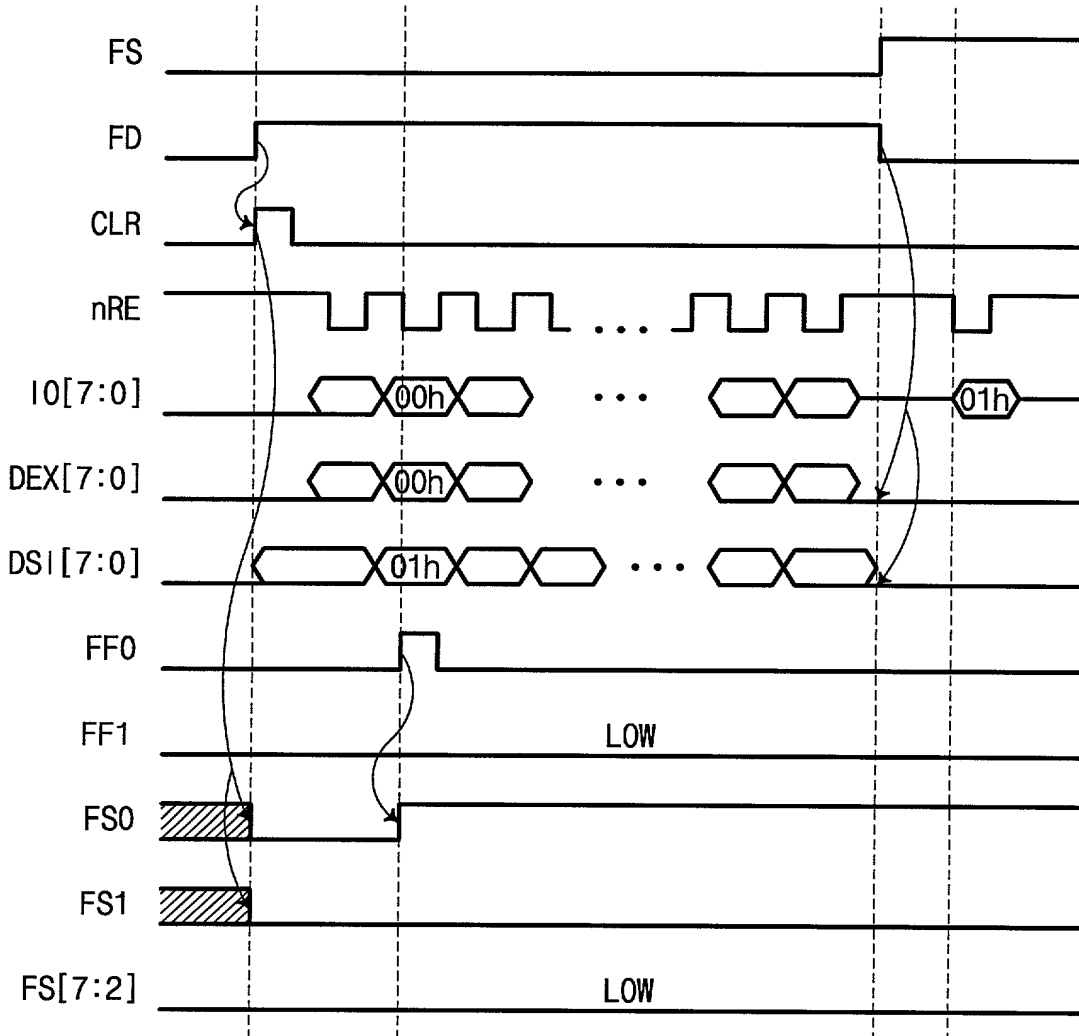


Fig. 13

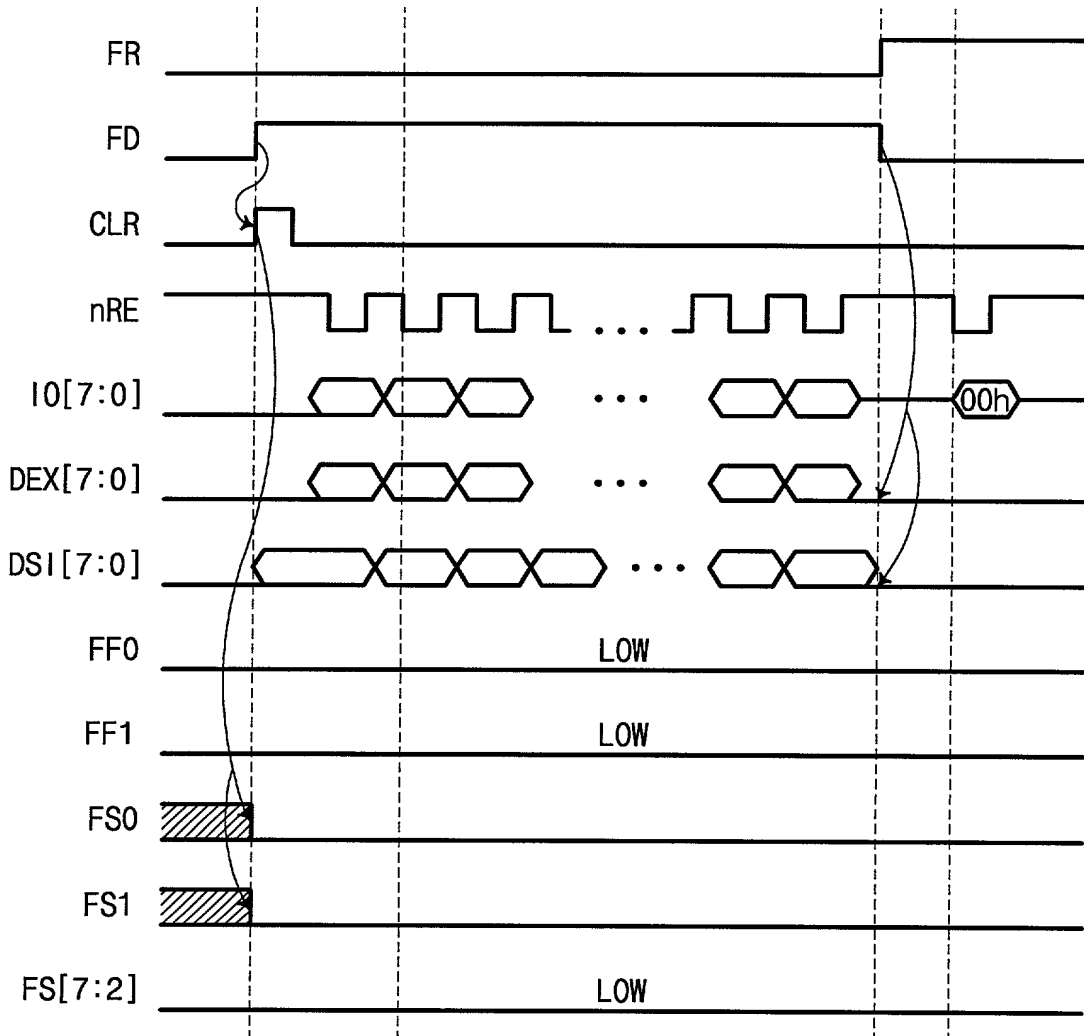


Fig. 14

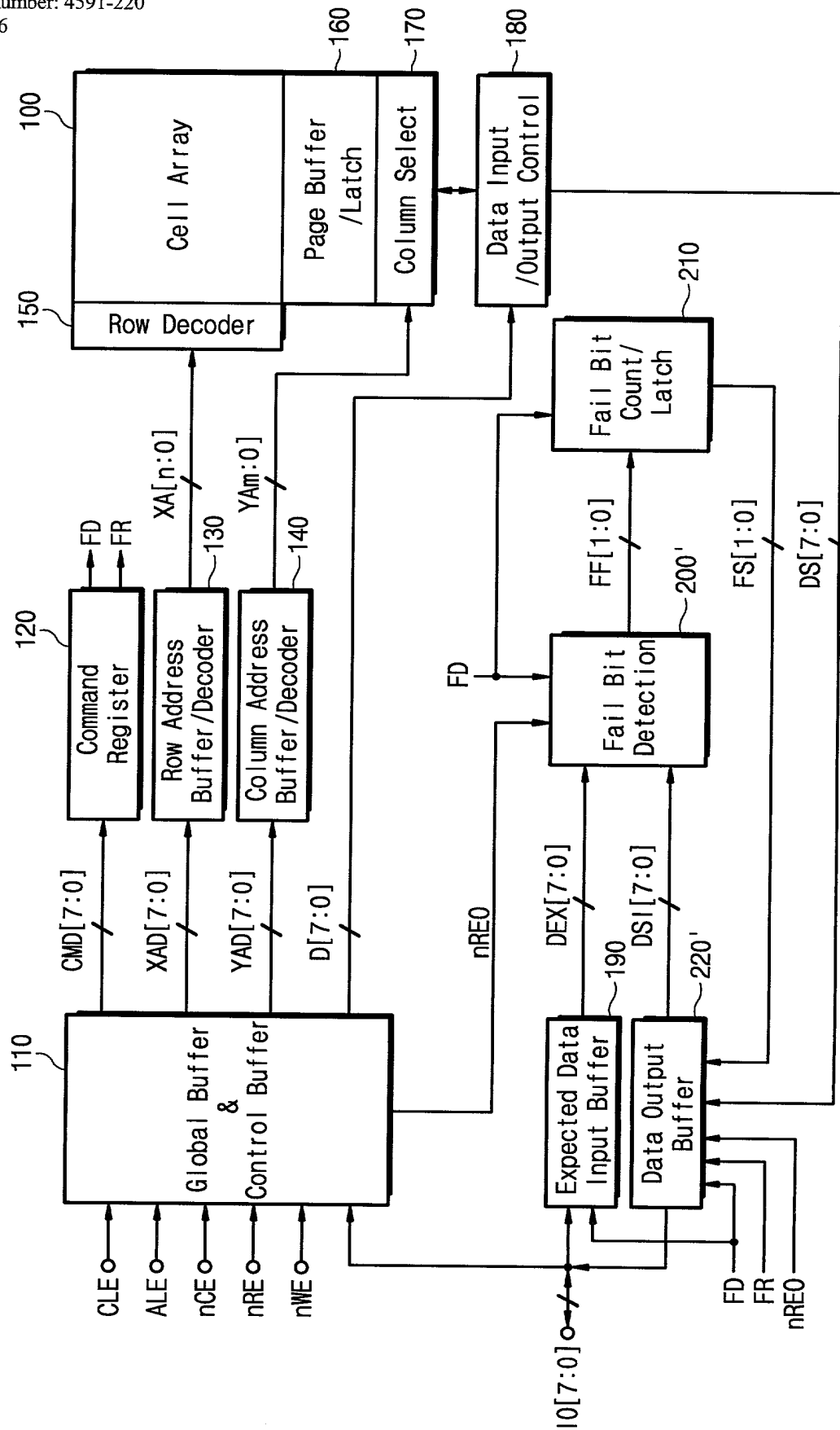


Fig. 15

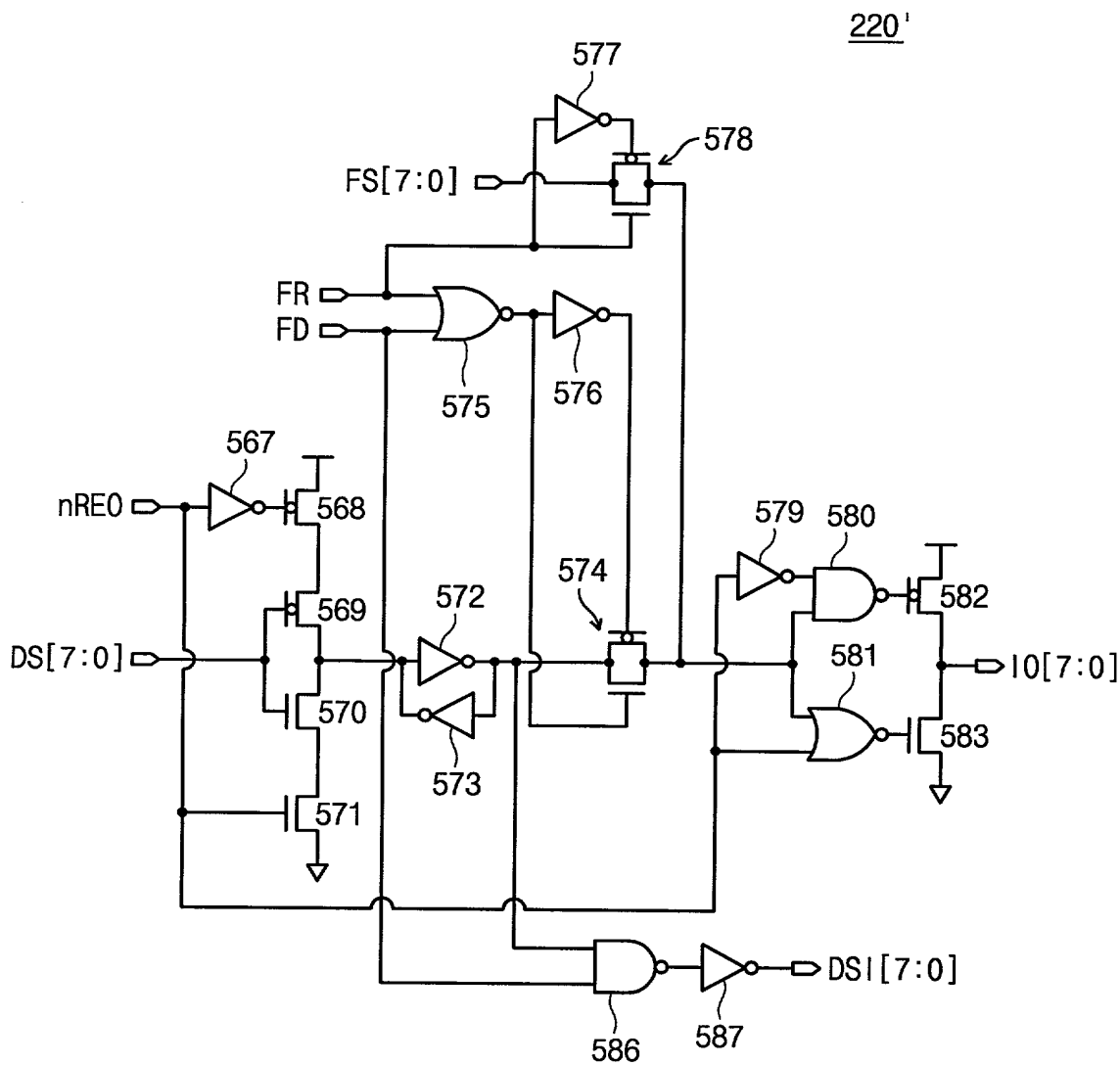


Fig. 16

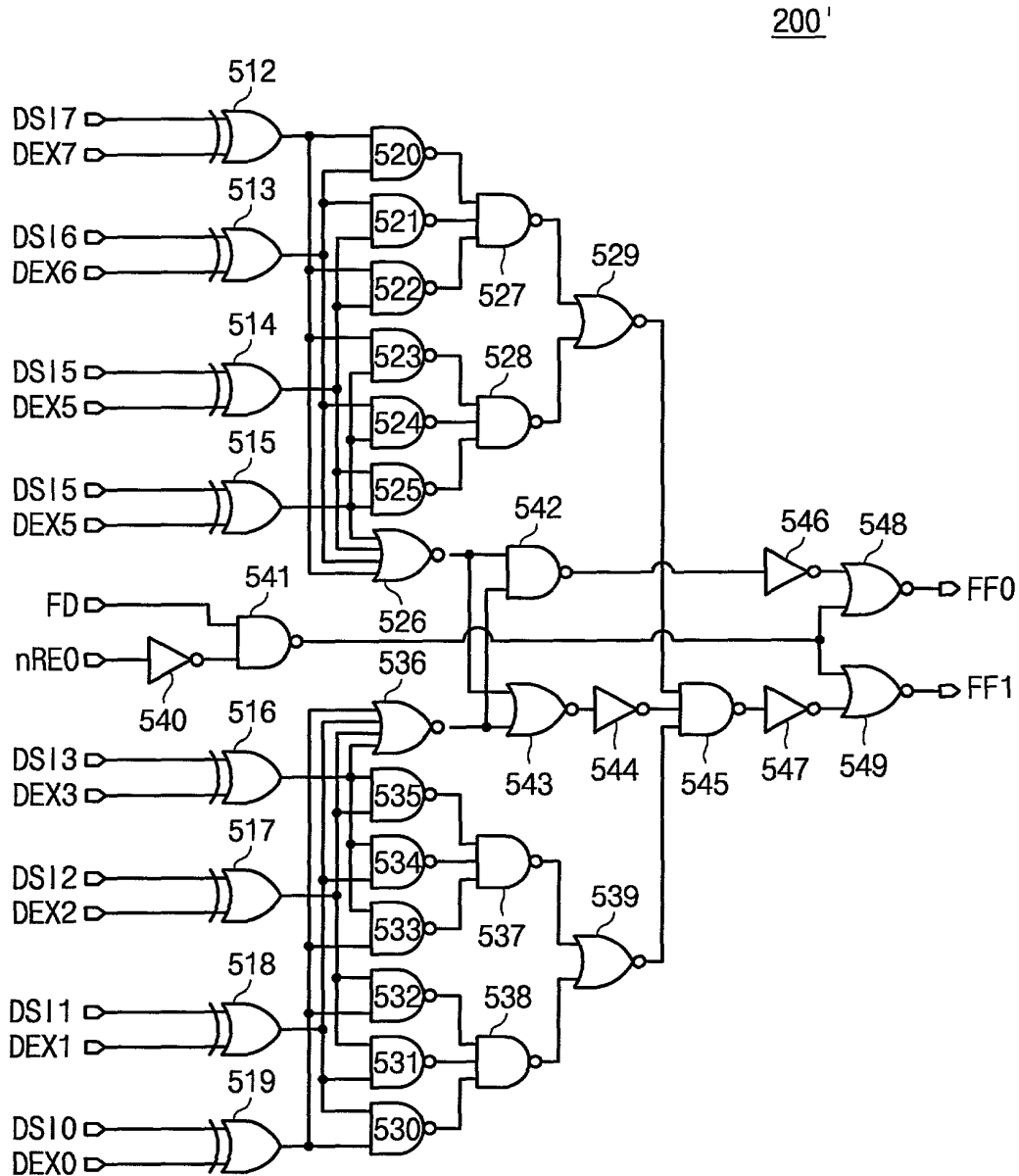


Fig. 17

1700

